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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

EARNSHAW et al

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Serial No. 10/751,108

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Filed: January 5, 2004

Examiner: unknown

For: MODELING INTEGRATED CIRCUITS

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July 6, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

STATUS INQUIRY

It is respectfully requested that a report on the status of this application be provided.

Respectfully submitted,

NIXON & VANDERHYE P.C.

By:



John R. Lastova
Reg. No. 33,149

JRL:sd

901 North Glebe Road, 11th Floor
Arlington, VA 22203-1808
Telephone: (703) 816-4000
Facsimile: (703) 816-4100